MULTILAYER CERAMIC CHIP CAPACITORS

Meritek

Selection of Ceramic Chip Capacitor

MERITEK Multilayer Ceramic Chip Capacitors offer the most complete range of characteristics and configurations available in the industry. We recommend that your selection of capacitors be based on consideration of the following items.

1. DIELECTRIC TYPE

The choice of dielectric is determined by the required capacitance-temperature stability. We offer COG (IB), X7R (2R1), Y5V, and Z5U (2E6)

2. CAPACITANCE AND TOLERANCE

Capacitance and its tolerance are determined by circuit requirements and cost considerations.

3. RATED VOLTAGE

Rated voltage is determined by circuit requirements.

4. SIZE

Size is determined by the circuit design and cost considerations.

5. PACKAGING

Specify the packaging formatof capacitors as bulk or tape & reeled.

6. NON-STANDARD REQUIREMENTS

Specify any non-standard requirements which are not stated in catalogue.

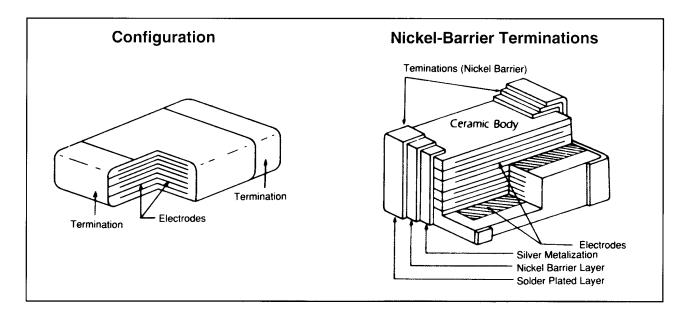
Dielectric	COG (1B)	X7R (2R1)	Z5U (2 E6) & Y5V (2 F4)
Features	 Ultra- stable Low dissipation factor Tight tolerance available Good frequency performance No aging of capacitance 	 Semi-stable High K Highly volumetric efficiency Highly reliable in high temperature application High insulation resistance 	 Highest volumetric efficiency Non-polar construction General purpose, High K
Applications	 LC and RC tuned circut Filtering Timing 	 Blocking Coupling Timing Bypassing Frequency discriminating Filtering 	BypassingDe-couplingFiltering

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MA Series Meritek

MERITEK Multilayer Ceramic Chip Capacitors are supplied in bulk or tape & reel packaging, They are ideal for thickfilm hybrid circuits and automatic surface mounting on any printed circuit board.

The nickel-barrier terminations consist of a nickel barrier layer over the silver metallization, finished with an electroplated solder layer to ensure that the terminations will have good solderability. The nickel barrier layer in terminations prevent the dissolution of termination when the parts are exposed to extended immersion in molten solder at elevated solder temperatures.



RESISTANCE TO SOLDERING

Termination Material	Code	Test Conditions
Nickel-barrier, Solder plated	N	260°C, 60 Sn/40 Pb solder, 60 secs.

TOLERANCES AVAILABLE

Dielectric			
EIA	IEC	Available Tolerance	Capacitance
COG	1BCG	±0.25 pF	≤ 5 pF
		±0.5 pF	5 pF < CAP. < 10 pF
		±5%, ±10%, ±20%	≥ 10 pF
X7R	2R1	±5%, ±10%, ±20%	All values
Z 5U	2E6	±20%, +80% –20%	All values
Y5V	2F4	±20%, +80% -20%	All values

[•] Other tolerances are available upon request.